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Patent PD-200335 Customer No. 020991

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Cocchi, Ronald P.

et al.

Serial No.:

10/085,331

Filed:

February 28, 2002

For

MULTIPLE NONVOLATILE

**MEMORIES** 

Group Art Unit: 2132

Examiner:

to be determined

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The reference cited on the enclosed form PTO/SB/08B may be material to the examination of the above-identified patent application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. In accordance with the recent Notice of the USPTO regarding the submission of Information Disclosure Statements without copies of the U.S. patents and published applications in patent applications filed after June 30, 2003, Applicant submits herewith copies of foreign patent documents and non-patent literature only, if applicable. It is respectfully requested that the references be considered and made of record in the application identified above.

This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, that additional information material to the examination of this application does not exist, or that this citation constitutes prior art.

It is believed that no fee is due with this submission. If a fee is due, the Commissioner is hereby authorized to charge any additional fees which may be required, or credit any

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Respectfully submitted,

Bv:

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use as many sheets as necessary)				Complete if Known			
				Application Number	10/085,331		
				Filing Date	February 28, 2002		
				First Named Inventor	Cocchi, Ronald P.		
				Art Unit	2132		
				Examiner Name	to be determined		
Sheet	1	of	1	Attorney Docket Number	PD-200335		

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	1	NON PATENT LITERATURE DOCUMENTS	<del> </del>
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		KHOURI, OSAMA, et al., "Low Output Resistance Charge Pump for Flash Memory Programming", IEEE International Workshop on Memory Technology, Design and Testing (MTDT '01), San Jose, U.S.A.,	
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